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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No.: 09/903,059 Confirmation No.: 9375  
First Named Inventor: Bulucea, Constantin Filing Date: 10 July 2001  
Group Art Unit: 2891 Examiner: Farhani, D.  
Atty. Docket No.: NS-4971 US  
Title: Gate-Enhanced Junction Varactor  
Assignee(s): National Semiconductor Corporation

Mountain View, California  
12 April 2006

**MAIL STOP AMENDMENT  
COMMISSIONER FOR PATENTS  
PO Box 1450  
Alexandria, Virginia 22313-1450**

**CORRECTION TO AMENDMENT**

Sir:

This is a correction to the Amendment submitted 11 April 2006 in response to the Office Action mailed 11 January 2006 for the above patent application. On page 18 of the 11 April 2006 Amendment, Applicant's Attorney stated that "A punctuation error has been corrected in Claim 109". Instead, Applicant's Attorney should have stated that Claim 109 has been amended to make the field-insulating-region limitation into a separate subparagraph, and thus a separate element of the claimed structure, since the field insulating region is available for use in semiconductor devices other than the recited varactor. The revised subparagraph arrangement of Claim 109 is then consistent with the subparagraph arrangements of independent Claims 17 and 23.

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**EXPRESS MAIL LABEL NO.:**

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Respectfully submitted,

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